

**Complete if Known**

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Application Number	10/674,604
Filing Date	09/30/2003
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Fiegle, Ryan Paul
Art Unit	2183
Attorney Docket No.	AUS920030478US1

[illegible]

**FOREIGN PATENT DOCUMENTS**

FOREIGN PATENT DOCUMENT				
Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON PATENT LITERATURE

<b>Examiner Initial</b>		Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
<b>RF</b>	<b>AC</b>	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

Examiner: /Ryan Fiegler/

Date Considered: 05/03/2006

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Substitute for form 1449/PTO

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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
RF	AA	4,034,353	07-05-1977	Denny et al.
	AB	4,821,178	04-11-1989	Levin et al.
	AC	5,151,981	09-29-1992	Westcott et al.
	AD	5,581,482	12-03-1996	Wiedenman et al.
	AE	5,594,864	01-14-1997	Trauben
	AF	5,691,920	11-25-1997	Levine et al.
	AG	5,758,168	05-26-1998	Mealey et al.
	AH	6,480,966 B1	11-12-2002	Rawson, III
	AI	2003/0135720 A1	07-17-2003	DeWitt et al.
	AJ	5,926,640	07-20-1999	Mason et al.
	AK	6,185,652 B1	02-06-2001	Shek et al.
	AL	4,145,735	03-20-1979	Soga
	AM	5,548,762	08-20-1996	Creedon et al.
	AN	4,794,472	12-27-1988	Doyama
	AO	6,161,187	12-12-2000	Mason et al.
	AP	3,707,725	12-26-1972	Dellheim
	AQ	6,408,386 B1	06-18-2002	Hammond et al.
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	AU	4,825,359	04-25-1989	Ohkami et al.
	AV	6,148,321	11-14-2000	Hammond

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RF	AW	Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 <sup>nd</sup> Ed., 1982, pp. 434-443.
RF	AX	Inoue, "Digital mobile communication system designed for nationwide police activities – WIDE system", IEEEExplore, October 1996, pp. 1-2.

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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
RF	AA	5,710,881	01-20-1998	Gupta et al.
	AB	5,822,763	10-13-1998	Baylor et al.
	AC	5,928,334	07-27-1999	Mandyam et al.
	AD	6,094,709	07-25-2000	Baylor et al.
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	AK	5,113,507	05-12-1992	Jaeckel
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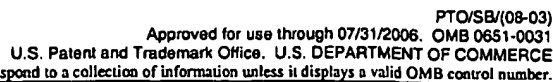
**NON-PATENT LITERATURE**

Examiner Initial		Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
RF	AN	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 <sup>nd</sup> Edition, pp. 10-12.
↓	AO	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
	AP	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.
	AQ	Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>
	AR	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, <a href="http://www.ibm.com">ip.com</a> , IPCOM000012728D, pp. 1-2
	AS	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html</a>

Examiner: /Ryan Fiegle/

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